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THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICATION OF:

Jurgen Ficker et al.

SERIAL NO:

10/562,989 🗸

GROUP ART UNIT:

Not assigned

FILED:

Dec. 28, 2005

EXAMINER:

Not assigned

CUSTOMER NO.:

27162

FOR:

Method and Device for Patterning Organic Layers

ATTY/DKT NO.:

411000-143

INFORMATION DISCLOSURE STATEMENT

MS Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

SIR:

Pursuant to 37 C.F.R. §1.56(a), Applicant(s) hereby cite(s) the enclosed documents listed on the attached copy of Form PTO-1449. Applicants make no admission that the cited references are prior art or that these references are in fact material to the patentability of the above-entitled application. The relevancy of the cited foreign language documents is that these were cited in copending applications or foreign counter part applications thereof all of which are generally related to organic semiconductor devices and method of making such as transistors, diodes, integrated circuits and the like.

This Information Disclosure Statement is filed in accordance with the paragraph of 37 CFR §1.97 indicated below:

X §1.97(b) This Information Disclosure Statement is filed:

- (1) Within three months of the filing date of a national application; OR
- (2) Within three months of the date of entry of the national stage of an international application; OR
- (3) Before the mailing of a first Office Action on the merits. No fee or statement is required.

§1.97(c) This Information Disclosure Statement is filed after the period specified in paragraph (b) above, but before the mailing date of either:

- (1) A Final Action or under 37 CFR §1.113; OR
- (2) A Notice of Allowance under 37 CFR §1.311; AND

is accompanied by either: (check one)

	The statement as specified in 37 CFR §1.97(e) set out below; OR
	The fee of \$180.00 under 37 CFR §1.17(p).
§1.97(d)	This Information Disclosure Statement is filed after the mailing date of either:
	(1) a Final Action or under 37 CFR §1.113; OR
	(2) A Notice of Allowance under 37 CFR §1.311;
	BUT filed on or before payment of the Issue Fee; AND
	is accompanied by:
	(1) The statement as specified in 37 CFR §1.97(e) as set forth below; AND
	(2) Petition is hereby made under 37 CFR §1.97(d) for consideration of this Information Disclosure Statement; AND,
	(3) The petition fee of \$180.00 set out in 37 CFR §1.17(i).
§1.97(e)	The undersigned Attorney hereby states that:
	each item of information contained in this Information Disclosure Statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing date of this Information Disclosure Statement; or
	no item of information contained in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application, or to the knowledge of the undersigned Attorney after making reasonable inquiry, was known to any individual designated in 37 CFR §1.56(c) more than three months prior to the filing date of the Information Disclosure
	Statement.

The Commissioner is authorized to charge payment of any fees associated with this communication or credit any overpayment to Deposit Account No. 03-0678.

FIRST CLASS MAIL CERTIFICATE Deposit Date: Feb. 1, 2006

I hereby certify that this paper and the attachments hereto are being deposited today with the US Postal Service as first class mail, postage prepaid, on the date indicated above, addressed to: MS Amendment

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Janice Speidel Date

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Respectfully submitted,

Jurgen Ficker et al.

BY William Squire Reg. No. 25,378

Attorney for Applicants

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Approved for use through 07/31/2006. OMB 0651-0031

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

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Complete if Known					
Application Number	10/562,989				
Filing Date	Dec. 28, 2005				
First Named Inventor	Jurgen Ficker				
Group Art Unit	Not Assigned				
Examiner Name	Not Assigned				
Attorney Docket Number	411000-143				

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				Group Art Unit	Not Assigned
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	STA	TEMENT BY API	PLICA	NT		First Na	amed Inventor	Jurgen F	icker	
						Group	Art Unit	Not Ass	igned	
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		• .		Group Art Unit	Not Assigned	
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				Application Number	10/562,989
	INFORMATION DISC	LOSU	RE	Filing Date	Dec. 28, 2005
	STATEMENT BY AP	PLICA	NT	First Named Inventor	Jurgen Ficker
				Group Art Unit	Not Assigned
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Substitute	Substitute for form 1449A/PTO			Сотр	Complete if Known	
				Application Number	10/562,989	
	INFORMA	TION DISCLOS	SURE	Filing Date	Dec. 28, 2005	
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Sheet	9	4110 00- 143	11	Attorney Docket Number	411000-143

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	INFORMATION DISC	LOSU	RE	Filing Date	Dec. 28, 2005	
	STATEMENT BY API	PLICA	NT	First Named Inventor	Jurgen Ficker	
				Group Art Unit	Not Assigned	
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				Application Number	10/562,989
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	STATEMENT BY APPLICANT			First Named Inventor	Jurgen Ficker
				Group Art Unit	Not Assigned
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#264824 v12 MASTER LIST III-includes 8/1/05 references



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICATION

Jurgen Ficker et al.

OF:

SERIAL NO:

10/562,989

GROUP ART UNIT:

Not assigned

FILED:

Dec. 28, 2005

EXAMINER:

Not assigned

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CUSTOMER NO.

27162

FOR:

Method and Device for Patterning Organic Layers

ATTY/DKT NO.:

411000-143

Mail Stop Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

DISCLOSURE STATEMENT UNDER 37 CFR 1.56

SIR:

This paper is to bring to the attention of the PTO the following commonly owned copending U.S. applications, all of which are related in different respects to organic electronic devices and/or method of making such devices such as transistors, diodes, integrated circuits and the like. Many of these applications also have one or more common inventors. The enclosed PTO 1449 lists these applications. It is respectfully requested that the Examiner consider and make of record all of the cited applications listed on the attached PTO 1449.

Application No.	<u>Title</u>	Inventors	Atty. Dkt. No.
10/332,140	Method for the Production and Configuration of Organic Field-Effect Transistors (OFET)	Adolf Bernds et al.	411000-103
10/344,951	Organic Field-Effect Transistor (OFET), A Production Method Therefor, An Integrated Circuit Constructed From the	Adolf Bernds et al.	411000-99

	Same and Their Uses		
10/362,932	Organic Field Effect Transistor, Method for Structuring an OFET and Integrated Circuit	Adolf Bernds et al	411000-110
10/380,113	Organic Rectifier, Circuit, RFID Tag and Use of an Organic Rectifier	Adolf Bernds et al.	411000-106
10/380,206	Organic Memory, Identification Marker (RFID-TAG) with Organic Memory and Uses of an Organic Memory	Adolf Bernds et al.	411000-102
10/381,032	Electrode and/or Conductor Track for Organic Components and Production Method Thereof	Adolf Bernds et al.	411000-105
10/433,959	Organic Field Effect Transistor, Method For Structuring an OFET and Integrated Circuit	Adolf Bernds	411000-108
10/433,961	Device For Detecting and/or Transmitting at Least One Environmental Influence, Method for Producing Said Device and Use Thereof	Wolfgang Clemens et al.	411000-111
10/467,636	Organic Field Effect Transistor With a Photostructured Gate Dielectric, Method for the Production and Use Thereof in Organic Electronics	Adolf Bernds et al.	411000-104
10/473,050	Device With At Least Two Organic Electronic Components and Method for Producing the Same	Adolf Bernds et al.	411000-113
10/479,234	Organic Field Effect Transistor, Method for Production and Use Thereof in the Assembly of Integrated Circuits	Adolf Bernds et al.	411000-101
10/479,238	Method For Producing Conductive Structures by Means of Printing Technique, and Active Components Produced Therefrom For Integrated Circuits	Adolf Bernds et al.	411000-100
10/492,922	Insulator for An Organic Electronic Component	Erwann Guillet et al.	411000-115
10/492,923	Electronic Unit, Circuit Design for the Same and Production Method	Wolfgang Clemens et al.	411000-114
10/498,610	Organic Field Effect Transistor with Offset Threshold Voltage and the Use Thereof	Walter Fix et al.	411000-119
10/508,640	Logic Component Comprising Organic Field Effect Transistors	Walter Fix et al.	411000-120

10/508,737	Device and Method for Laser Structuring Functional Polymers and	Adolf Bernds et al.	411000-121
10/517,750	Substrate for an Organic Field Effect Transistor, Use of the Substrate, Method		411000-122

	of Increasing the Charge Carrier Mobility and Organic Field Effect Transistor (OFET)		
10/523,216	Electronic Component Comprising Predominantly Organic Functional Materials And A Process For The Production Thereof	Adolf Bernds et al.	411000-123
10/523,487	Electronic Device	Wolfgang Clemens et al.	411000-124
10/524,646	Organic Component for Overvoltage Protection and Associated Circuit	Walter Fix et al.	411000-127
10/533,756	Organic Electronic Component with High- Resolution Structuring and Process for the Production Thereof	Wolfgang Clemens et al.	411000-128
10/534,678	Measuring Apparatus for Determining an Analyte in a Liquid Sample	Wolfgang Clemens et al.	411000-129
10/535,448	Organic Electronic Component Comprising Semi-Conductive Functional Layer and Method for Producing Said Component	Wolfgang Clemens et al.	411000-131
10/535,449	Organic Electronic Component Comprising the Same Organic Material for at Least Two Functional Layers	Adolf Bernds et al.	411000-132
10/344,926	An Electronic Circuit Having an Encapsulated Organic-Electronic Component, and a Method for Making an Encapsulated Organic-Electronic Component	Wolfgang Clemens et al.	411000-133
10/541,815	Organo-Resistive Memory Unit	Axel Gerlt et al.	411000-136
10/541,956	Board or Substrate for an Organic Electronic Device and Use Thereof		
10/541,957	Organic Field Effect Transistor And Integrated Circuit	Walter Fix et al.	411000-138
10/543,561	Organic Storage Component and Corresponding Triggering Circuit	Organic Storage Component and Wolfgang Clemens et	
10/542,678	Organic Electronic Component and Method For Producing Organic Electronic Devices	Adolf Bernds et al.	411000-140
10/542,679	Use of Conductive Carbon Black/Graphite Mixtures for the Production of Low-Cost Electronics	Adolf Bernds et al.	411000-141
10/562,989	Method and Device for Patterning Organic Layers	Jurgen Ficker et al.	411000-143
10/562,869	Logic Gate with a Potential-free Gate Electrode for Organic Integrated Circuits	Wolfram Glauert et al.	411000-144

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Respectfully submitted, Jurgen Ficker et al.

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Substitute for form 1449A/PTO			Com	Complete if Known		
INFORMATION DISCLOSURE			Application Number	10/562,989		
			Filing Date	Dec. 28, 2005		
(Use as many sheets as necessary)		First Named Inventor	Jurgen Ficker			
		Group Art Unit	Not assigned			
		Examiner Name	Not assigned			
Sheet 1 2		Attorney Docket Number	411000-143			

			U.S. PATENT DOC	UMENTS	
Examiner Initial*	Cite No. ¹	Document Number Number-Kid Code ^{2 (if known)}	Publication- Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
	103	US-6,852,583	10/09/2003	Adolf Bernds et al.	See accompanying Disclosure Statement filed herewith
	102	US-6,903,958	03/21/2002	Adolf Bernds et al.	
	133	US-10/344,926	02/12/2004	Adolf Bernds et al.	
	99	US-10/344,951	02/12/2004	Adolf Bernds et al.	
	110	US-10./362,932	10/02/2003	Adolf Bernds et al.	
	106	US-10/380,113	09/25/2003	Adolf Bernds et al.	
	105	US-10/381,032	02/12/2004	Adolf Bernds et al.	
	108	US-10/433,959	04/01/2004	Adolf Bernds	
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	104	US-10/467,636	11/04/2004	Adolf Bernds et al.	•
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	122	US-10/517,750	N/A	Wolfgang Clemens et al.	
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Substitute for form 1449A/PTO		Com	Complete if Known	
INFORMATION DISCLOSURE			Application Number	10/562,989
			Filing Date	Dec. 28, 2005
STATEMENT BY APPLICANT		First Named Inventor	Jurgen Ficker	
			Group Art Unit	Not assigned
(Use as many sheets as necessary)		Examiner Name	Not assigned	
Sheet 2 2		Attorney Docket Number	411000-143	

	129	US-10/534,678	N/A	Wolfgan	g Clemens et al.	
	131	10/535,448	N/A	W. Clemens et al.		
	132	10/535,449	N/A	Walter F	ix et al.	
	136	US-10/541,815	N/A	Axel Ge	rlt et al.	
	137	US-10/541,956	N/A	Wolfgan	g Clemens et al.	
	138	US10/541,957	N/A	Walter Fix et al.		
	139	US-10/543,561	N/A	Wolfgang Clemens et al.		
	140	US-10/542,678	N/A	Adolf Bernds et al.		ı
	141	US-10/542,679	N/A	Adolf Be	rnds et al.	
	143	US-10/562,989	N/A	Jurgen Ficker et al.		
	144	US-10/562,869	N/A	Wolfram	Glauert et al.	
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